



# Birla Institute of Technology & Science, Pilani Hyderabad Campus

Dr. Chetan Kumar Vudadha, Assistant Professor, EEE

## **CALL FOR JUNIOR RESEARCH FELLOW (JRF) FOR A DST-SERB PROJECT**

Date: 23<sup>th</sup> August, 2022

Applications are invited from suitable candidates for a **Junior Research Fellow (JRF)** position to work on the following project sponsored by **Science and Engineering Research Board (SERB)** file No. **SRG/2020/000202** under the supervision of **Dr. Chetan Kumar Vudadha, Assistant Professor, Department of Electrical and Electronics Engineering, BITS-Pilani, Hyderabad campus.**

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<b>Title of the project:</b>	A Novel 2:1 Multiplexer based Approach to Implement Ternary Logic Circuits
<b>Duration of the project:</b>	6 Months
<b>Project position:</b>	Junior Research Fellow (JRF)
<b>Number of Positions:</b>	One
<b>Fellowship amount:</b>	31,000/- per month
<b>Eligibility requirements:</b>	M.E. / M. Tech (in VLSI Design/Microelectronics or allied areas). [Should have been selected for Post Graduate (Masters) program through one of the following] a. Selection through CSIR-UGC NET or GATE (Should have qualified in the Past) b. Selection process through national level examinations conducted by central Government Departments and their Agencies and Institutions.
<b>Desired Experience:</b>	Prior exposure/experience in SPICE level coding and simulation of VLSI circuits is desirable

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**Interested candidates with the above mentioned qualification can fill details and upload their CV in the google form (<https://forms.gle/3QPMim1FUM8qGdwGA>) by 29th August 2022 5PM.** The CV should contain detailed information about the educational qualifications, research experience and published papers (if any), photograph and scanned copies of documents including- support of date of birth, educational qualifications (BE/B.Tech, ME/M.Tech), experience certificates from prior employment/project work (if any).

Please note that only qualified and suitable candidates will be notified for Interview via email. Interview would be through Google Meet.

Best Regards,  
Dr. Chetan Kumar V  
EEE Department, BITS Pilani Hyderabad Campus



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